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APPLICANTS

Tomomi Momohara, Yokohama-shi, JAPAN;

** CONTINUING DATA *****

This application is a CON of 09/511,620 02/23/2000 PAT 6,750,527
 which is a CON of 08/865,371 05/29/1997 PAT 6,055,655

** FOREIGN APPLICATIONS *****

JAPAN 8-136892 05/30/1996
 JAPAN 9-113900 05/01/1997

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 07/08/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	52	1	1
Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>				

ADDRESS

000026021
 HOGAN & HARTSON L.L.P.
 500 S. GRAND AVENUE
 SUITE 1900
 LOS ANGELES, CA
 90071-2611

TITLE

Semiconductor integrated circuit device and method of testing the same

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)

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